

FIG. 1

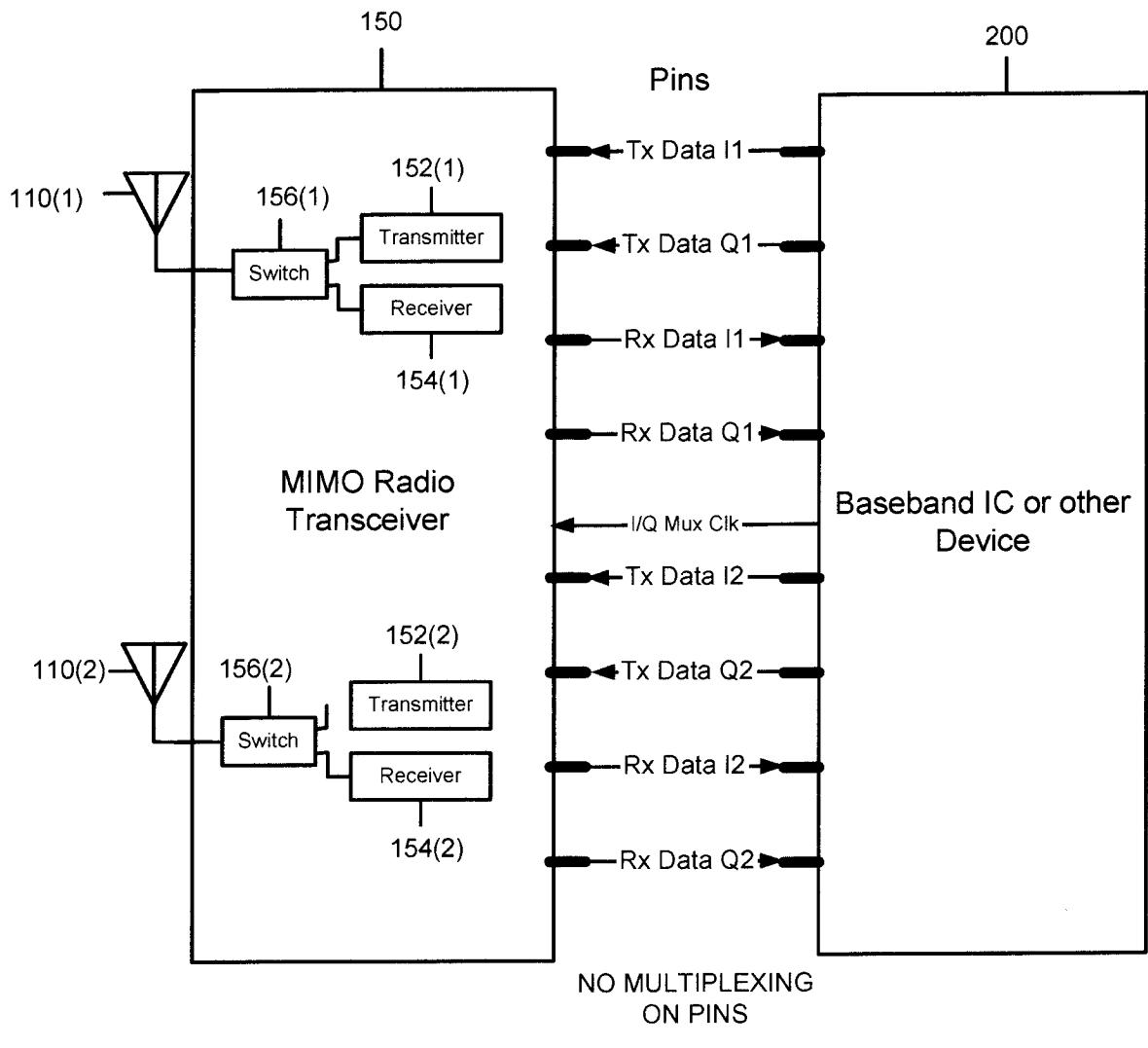


FIG. 2

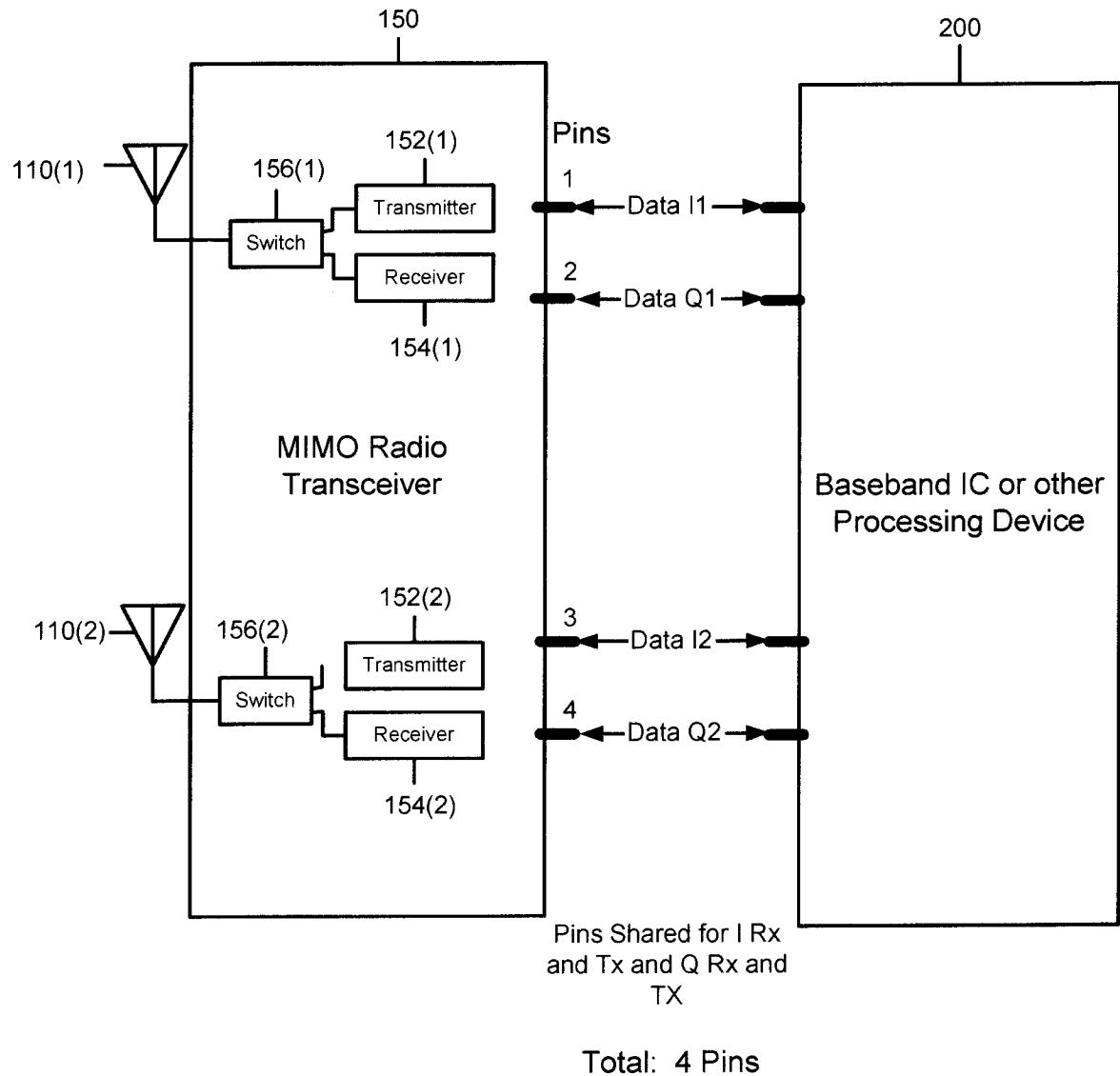


FIG. 3

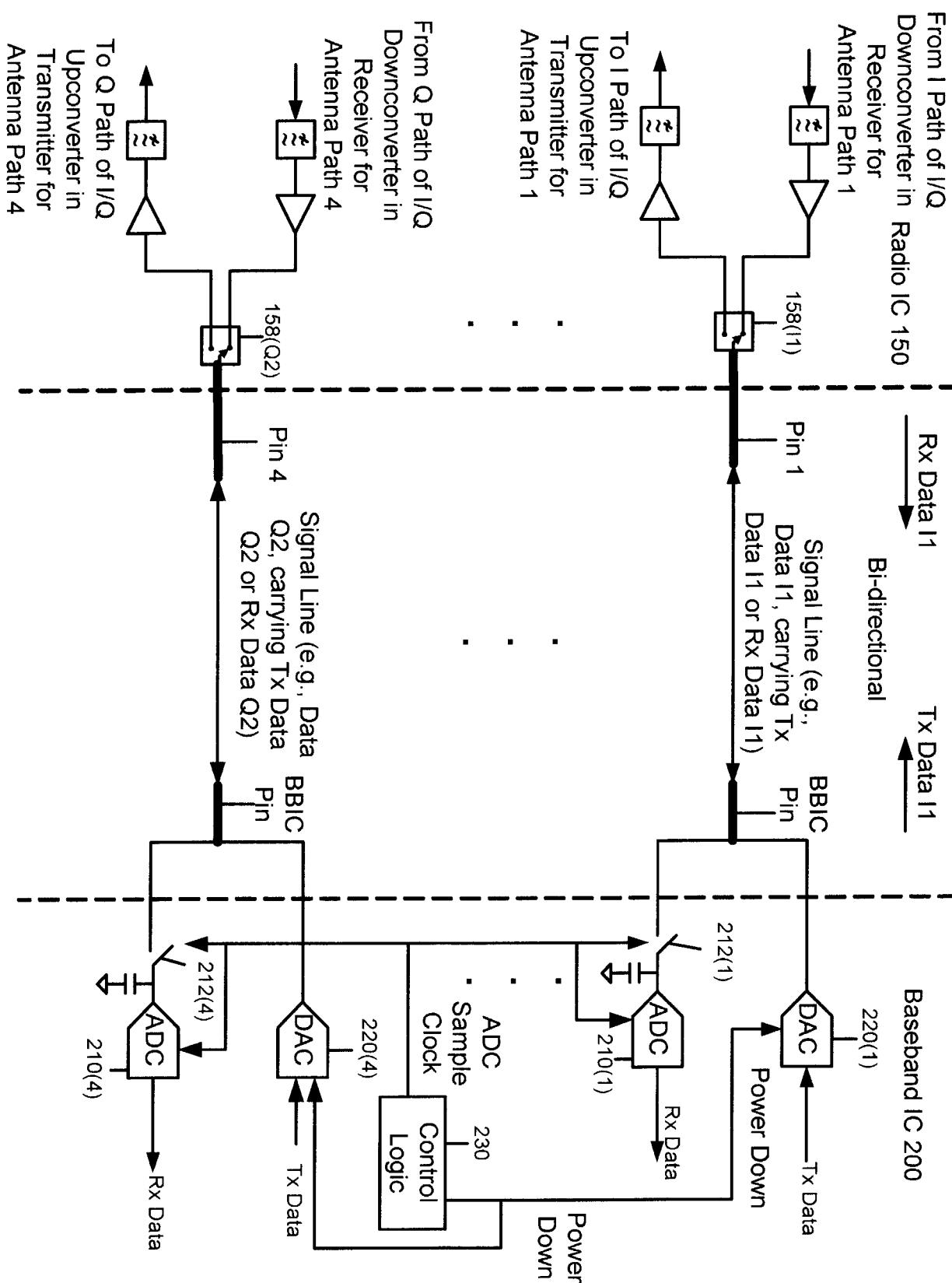


FIG. 4

RX/TX Mode	RX Mode	TX Mode	RX Mode
ADC Sample Clock			
Low = Hold			
DAC Power Down			
Low = Active			

FIG. 5

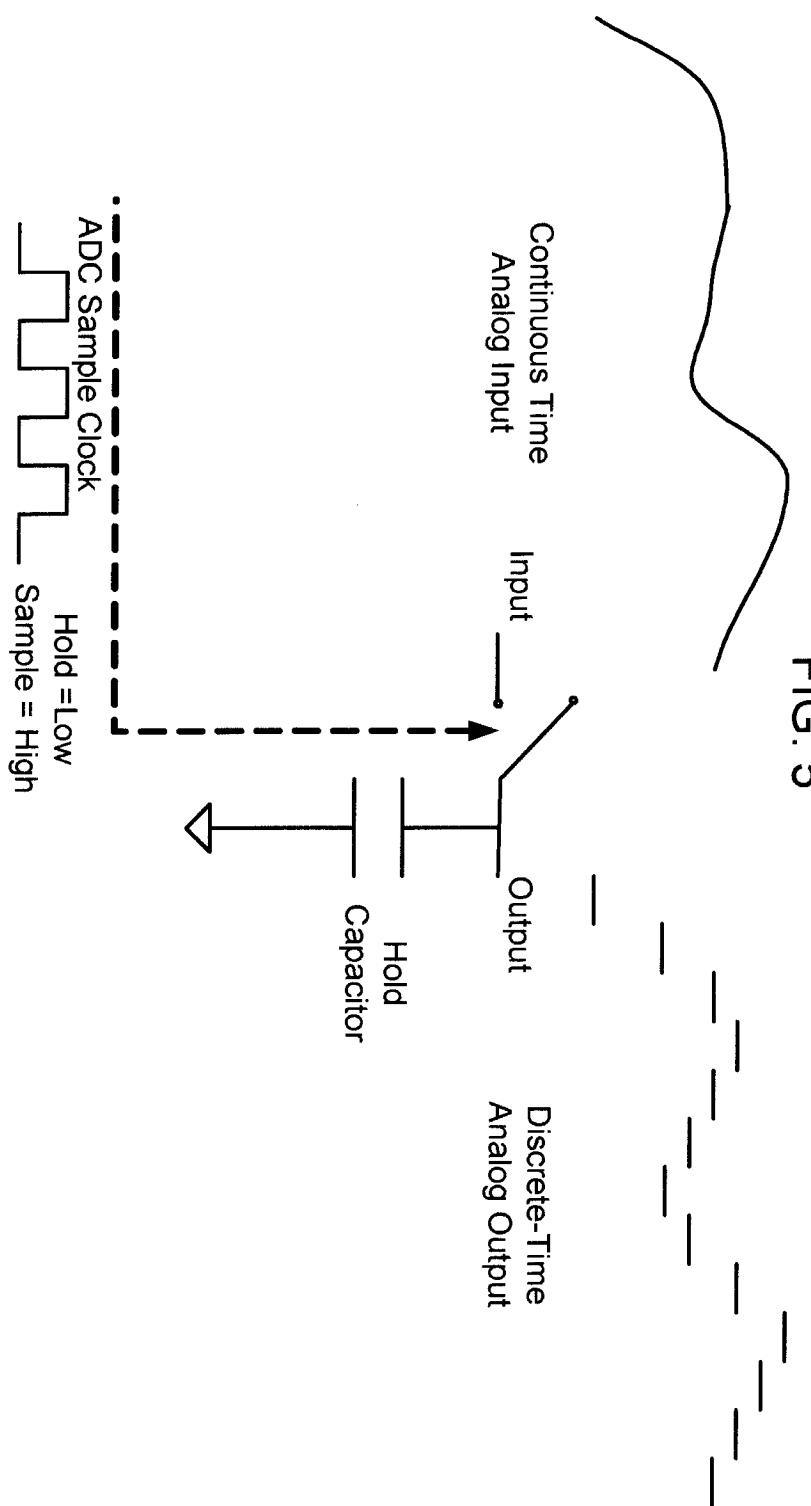


FIG. 6

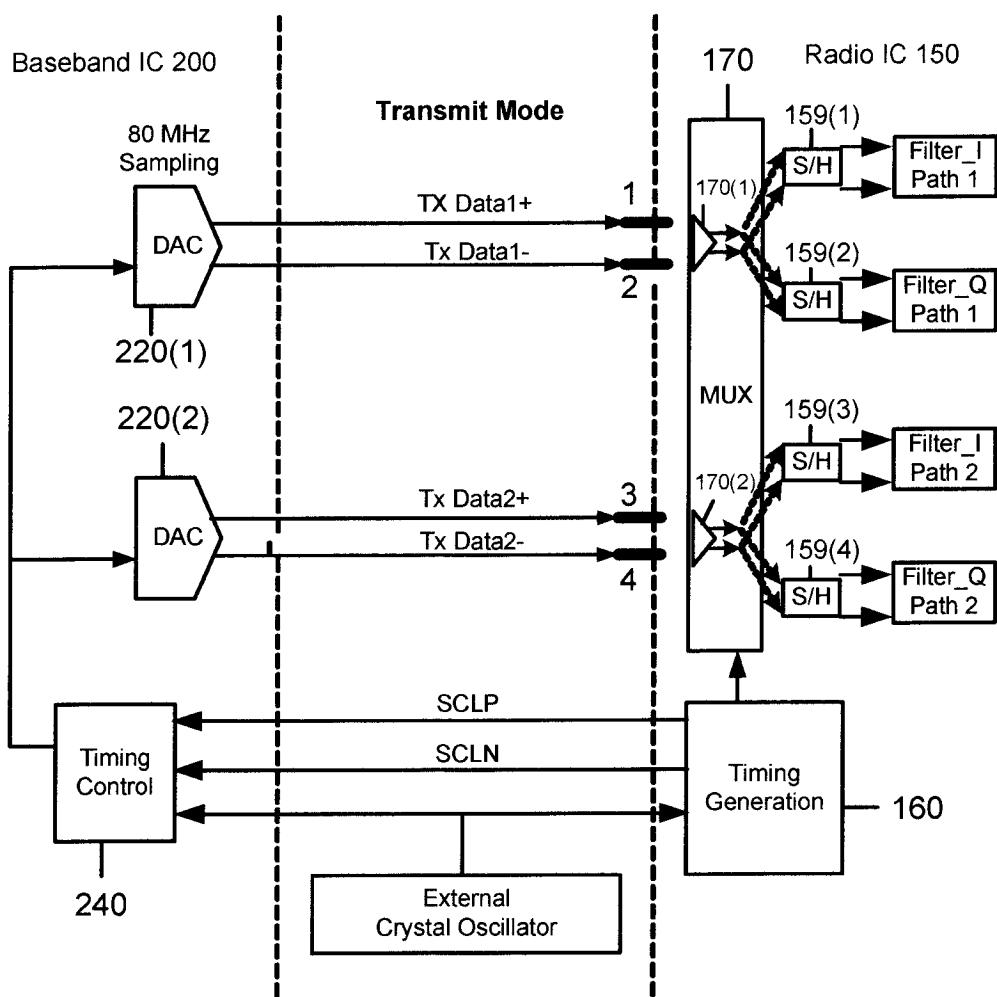


FIG. 7

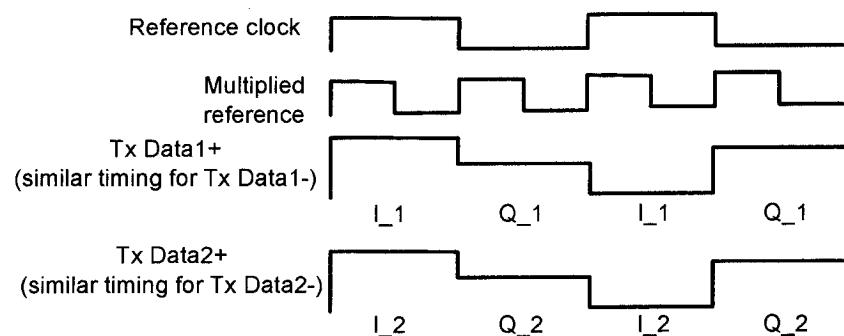


FIG. 8

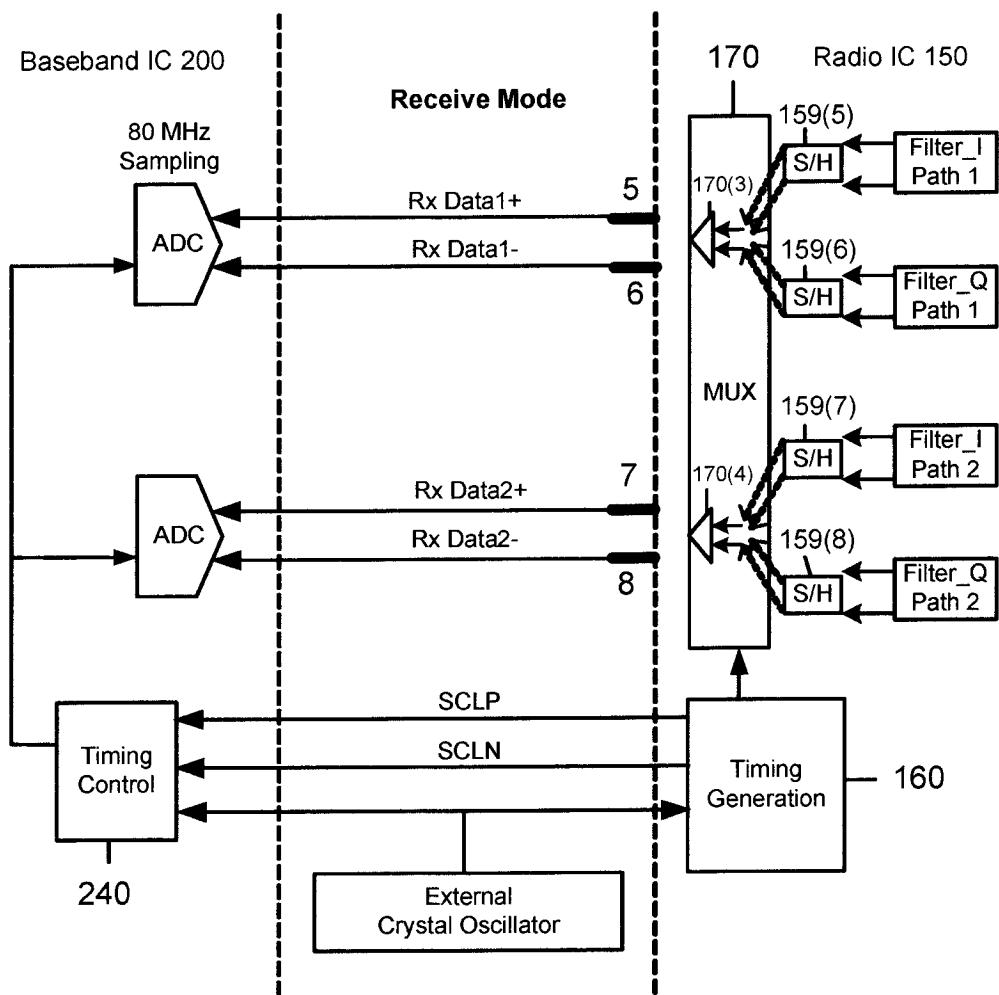


FIG. 9

